

IFW

03500.101486.

PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:)
TOMONARI NAKAYAMA, et al.)
Application No.: 10/559,799)
Int'l. Filing Date: June 9, 2005)
For: FIELD EFFECT TRANSISTOR)
AND PRODUCTION PROCESS)
THEREOF) November 30, 2006

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Sir:

In compliance with the duty of disclosure under 37 C.F.R. § 1.56 and in accordance with the practice under 37 C.F.R. §§ 1.97 and 1.98, the Examiner's attention is directed to the article listed on the enclosed Form PTO-1449. This article is discussed in the specification at page 3. A copy of the listed article, together with a partial English-language translation, is enclosed.

This Information Disclosure Statement is being filed before the issuance of a first Office Action on the merits. Therefore, no fee under 37 C.F.R. § 1.97(c)(2) is believed due. Nevertheless, the Commissioner may charge Deposit Account No. 06-1205, should any fee be due for filing this paper.

ALL REFERENCES CONSIDERED EXCEPT WHERE LINED THROUGH. /B.J./

Applicants request that the above information be considered by the Examiner and that a copy of the enclosed Form PTO-1449 be initialed and returned indicating that such information has been considered.

Applicants' undersigned attorney may be reached in our Costa Mesa, California office at (714) 540-8700. All correspondence should continue to be directed to our below-listed address.

Respectfully submitted,



Damond E. Vadnais

Attorney for Applicants
Registration No.: 52,310

FITZPATRICK, CELLA, HARPER & SCINTO
30 Rockefeller Plaza
New York, New York 10112-3800
Facsimile: (212) 218-2200

FORM PTO 1449 (modified)

U.S. DEPARTMENT OF COMMERCE
PATENT AND TRADEMARK OFFICELIST OF REFERENCES CITED BY APPLICANT(S)
(Use several sheets if necessary)ATTY DOCKET NO.
03500.101486.

APPLICATION NO.

10/559,799

APPLICANT

TOMONARI NAKAYAMA, et al.

INT'L. FILING DATE:

June 9, 2005

GROUP 2814

U.S. PATENT DOCUMENTS

*EXAMINER
INITIALDOCUMENT
NUMBER

DATE

NAME

CLASS

SUBCLASS

FILING DATE
IF APPROPRIATE

FOREIGN PATENT DOCUMENTS

DOCUMENT
NUMBER

DATE

COUNTRY

CLASS

SUBCLASS

TRANSLATION
YES/NO/
OR ABSTRACT

OTHER DOCUMENT(S) (Including Author, Title, Date, Pertinent Pages, Etc.)

/B.J./

Tatsuya Shimoda, et al., "Organic transistor fabricated by ink-jet printing", Appl. Phys. Lett., Vol. 70, 2001, pp. 1452-1456. (with partial translation)

03/24/2008

EXAMINER /Bilkis Jahan/

DATE CONSIDERED

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

Sheet 1 of 1

Form #61

CA_MAIN 124124v1

ALL REFERENCES CONSIDERED EXCEPT WHERE LINED THROUGH. /B.J./